

SPECIFICATION AMENDMENT

Please amend the paragraph beginning at page 2, line 27 as follows:

“Figure 4 shows an alternative embodiment of the present invention for generating a pulse width modulated (PWM) signal. The system includes a programmable pulse width modulator circuit 401, a plurality of delay elements 402-405, a delay locked loop [[4-6]] 406 similar to that of Figure 2, a selector 408 for selecting one of inputs a_0 through a_n to the selector to convey to the selector output, and a logic gate 409 connected to the selector output. In operation, the programmable PWM outputs a PWM signal of a fixed duty cycle set in accordance with instructions from microprocessor 14. The waveform that is output by programmable PWM generator 401 is shown as a_0 in Figure 5. In accordance with the inventions, the delay locked loop 406 maintains the delay of one full clock cycle through delay elements 402 – 405. Accordingly, each output is delayed by $1/n$ of the clock frequency, T_{clk} .”